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APR 11 2005

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JURGEN VOLLRATH
588 SUTTER STREET # 531
SAN FRANCISCO, CA 94102

04/12/2005 WABDELR3 00000009 10079336

01 FC:1501 1400.00 OP
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JURGEN VOLLRATH		(Depositor's name)
<i>J. Vollrath</i>		(Signature)
		4/19/05
		(D)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/079,336	02/19/2002	Vladislav Vashchenko	P05143	7946

TITLE OF INVENTION: ESD PROTECTION METHODS AND DEVICES USING ADDITIONAL TERMINAL IN THE DIODE STRUCTURES

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$0	\$1400	04/11/2005
EXAMINER	ART UNIT	CLASS-SUBCLASS		04/13/2005 AWONDAF2 00000089 140448	10079336
BENENSON, BORIS	2836	361-056000		01 FC:8001	9.00 DA

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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1 *JURGEN VOLLRATH*
2 _____
3 _____

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PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed in accordance as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

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National Semiconductor Corp

*2900 Semiconductor Dr
M/S 03-579, Santa Clara, CA, 95051*

Please check the appropriate assignee category or categories (will not be printed on the patent): Individual Corporation or other private group entity Government

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